a first mirror leg coupled between a supply voltage and the first input of the compensation circuit;

a second mirror leg coupled between the supply voltage and the first input of the reference voltage circuit; and

a third mirror leg coupled between the supply voltage and a first input of the reference voltage circuit.

REMARKS

Claims 1-19 will be pending upon entry of the present amendment. Claims 5-6 are being amended. Calims 8-19 are being newly presented.

Attached hereto is a marked-up version of the changes made to the specification and claims by the current amendment. The attached page is captioned **"Version With Markings to Show Changes Made."**

All of the claims remaining in the application are now clearly allowable. Favorable consideration and a Notice of Allowance are earnestly solicited.

Respectfully submitted,

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RXI:km

Enclosure: Appendix

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VERSION WITH MARKINGS TO SHOW CHANGES MADE

Please amend claims 5-6 as follows.

5.   (Amended)   The thermal sensor circuit of claim 3 ~~or claim 4,~~ wherein each of the first to sixth BJTs is an n-p-n transistor.

6.   (Amended)   The thermal sensor circuit of claim 3 ~~or claim 4,~~ wherein the current gain is given by:

$$\frac{I2}{I1} = \frac{\beta^2 + (3+N)\beta}{\beta^2 + \beta + (2+N)}$$

where:

$I1$ is the first current input;

$I2$ is the second current input; and

β is the common-emitter current gain of each of the first to sixth BJTs.